

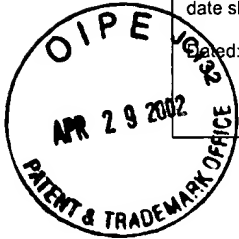
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Dated: April 29, 2002 Signature: *Michael J. Scheer*

(Michael J. Scheer)

Docket No.: X2850.0015/P015
(PATENT)

S/A
A. W. M. S.
5/8/02



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yoshiaki Shiota, et al.

Application No.: 09/220,434

Group Art Unit: 2662

Filed: December 24, 1998

Examiner: T. Tran

For: FRAME-RELAY FRAME TRANSMISSION
CIRCUIT

AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
Washington, DC 20231

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Dear Sir:

In response to the Office Action dated December 27, 2001 (Paper No. 4), please amend the above-identified U.S. Patent application as follows:

In the Claims

1. (Amended) A frame-relay frame processing device for reassembling a frame-relay frame into an Asynchronous Transfer Mode (ATM) cell, wherein said device receives a frame-relay frame and writes said frame to a memory location shifted from an end of the next available memory location in the frame buffer.

2. (Amended) A frame-relay frame processing device according to claim 1, wherein a size of the shift from an end of the next available memory location is determined for each connection.